



# V Rose Microsystems, Inc.

www.vrosemicrosystems.com

## VRM-AMC100

# Product Data Sheet

### Overview:

The **VRM-AMC100** is a double-width, full-height module based on the AMC.1 Specification. The AMC100 allows PMC or PrPMCs to be installed in an AMC slot. The PMC/PrPMC PCI-X bus runs at 133MHz. The J4 connector of the PMC/PrPMC is routed to the front panel of the AMC module. For PMCs and PrPMCs that are PICMG 2.15 compliant, the Gigabit Ethernet ports are routed to the AMC connector per the AMC.2 specification. This modular approach allows an AdvancedTCA chassis to utilize the large numbers of PrPMC modules as well as PMC I/O modules that are available in the market. The AMC100 can be configured to run in non-transparent, transparent or root complex mode. **V Rose Microsystems can modify this product to meet special customer requirements.**



### Features:

- Support for PMC and PrPMC modules
- 64-bit PCI-X @133MHz
- AMC.1 and AMC.2 compliant
- PCIe x4 lanes
- Transparent or Non-Transparent operating modes
- PMC J4 connector routed to front panel Mini-SCSI type connector or Gigabit transceiver to AMC.2
- IPMI 2.0 compliant Module Management Controller (MMC)
- 32-bit IPMI RISC processor
- IEEE Std P1386.1-2001 (PMC) compliant
- ANSI/VITA 32-2003 (PrPMC) compliant
- RoHS compliant

### POWER

AMC100: 3 Watts without PMC/PrPMCs  
PMC/PrPMC Power: +3.3V @ 5 A, +5V @ 5 A

### ENVIRONMENTAL

Temperature: Operating Temperature 0C to 65C  
(Air flow requirement is to be greater than 200 LFM)  
Storage Temperature: -40C to 90C  
Vibration: 1 G, 5-500Hz each axis  
Shock: 30Gs each axis  
Relative Humidity:: 5 to 95 percent, non-condensing

### FRONT PANEL

Interface Connectors: Mini SCSI Type Connector  
LEDs: IPMI Management Control, PCIe x4 lanes, Ethernet activity  
Mechanical: Hot Swap Ejector Handle

### SOFTWARE

Operating Systems: Linux, Windows, Solaris, VxWorks

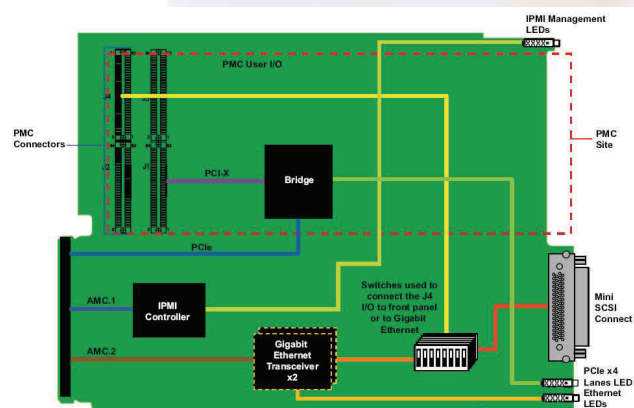


FIGURE 1. AMC100 Functional Block Diagram

### Ordering:

**VRM-AMC100:** AMC Site Carrier for PMC/PrPMC