

Overview:

The **VRM-IP360** is an intelligent high performance Ethernet Industry Pack module based on the MC68EN360 Quad Integrated Communication Controller (QUICCTM). It has been designed to support high data transfer rates with a minimum of impact on the system load. The MC68EN360 is a versatile one-chip integrated microprocessor and peripheral combination that can be used in a variety of controller applications. The internal CPU32+ processor (4.5MIPS at 25MHz) can handle various operating systems and communication protocol software. The **VRM-IP360** thus enables intelligent and embedded Ethernet communications on IP based systems. The **VRM-IP360** can be optionally delivered with 2 Ethernet interfaces using the two Ethernet channels of the QUICC. The **VRM-IP360** supports all of today's standard protocols (TCP/IP, DECNet, ISO/OSI protocol) and is prepared for tomorrow's demands. All of our network protocols are based on our Universal Protocol Stack Architecture (UPSA) which supports the simultaneous and independent execution of different network protocols. For our protocol stack, a driver for OS-9 and a firmware interface is available. The firmware interface enables the customer to implement host interfaces to different operating systems or integrate the **VRM-IP360** in systems running without an operating system.

The MC68EN360 handles all of the local network protocol up to layer 4 (socket interface). Thus, the system's main processor is free and real-time capability of the system is undiminished by even high network traffic. The fast multitasking Open Kernel (OK1) is responsible for handling the on-board firmware. A developer's kit for the OK1 enables the user to link customer specific application software to the OK1, running simultaneously to the protocol stack. The **VRM-IP360** consists of the IP module and the transition module. The transition module carries the network interfaces (1 or 2).

Features:



Technical Data Module:

- MC68360 Integrated Multi-protocol Communication Processor (QUICC)
- 1 or 2 Ethernet Interfaces
- 1 or 4 Mbyte dual ported DRAM
- 512Kbyte Flash EPROM
- Single size IP Module
- OS-9 driver (drivers for different operating systems on request)
- Firmware-Interface
- Protocol Software On-board (TCP/IP, ISO/OSI)

Technical Data Transition Module:

- Single Euro size or Double Euro size
- Single or dual slot front panel
- AUI
- Optionally BNC (Cheapernet)
- Twisted Pair

Technical Details:

- IP-Module Bus Interface: IP Bus, Rev. 1.0, D16/A8, D16/A22
- Processor: MC68EN360 (QUICC), 25 MHz
- Memory: 1 MByte dual-ported DRAM as communication RAM (internal 32 bit bus width)/ Optionally 4MB DRAM as communication RAM; 512KByte Flash PROM
- Network: 1 or 2 Ethernet Ports
- Protocols: Concurrent handling of different protocols; TCP/IP, ISO/OSI
- Drivers: OS9
- Connectors: Connector for the signals of the communications interfaces.