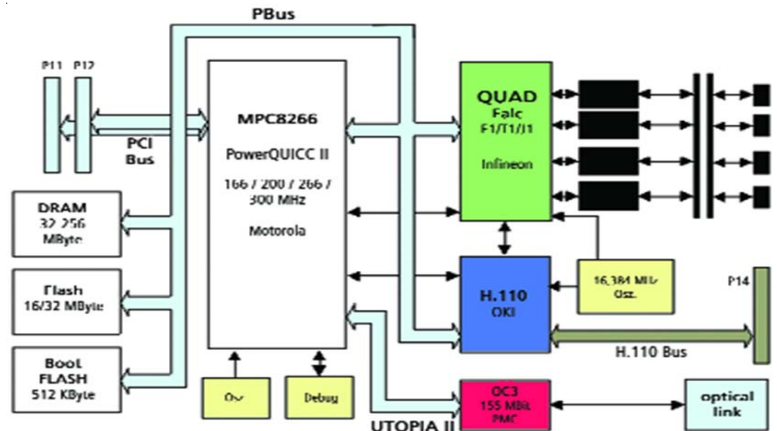


Overview:

The **VRM-PMC-8266-OC3** is a telecommunications interface board in PMC form factor. Based on the Motorola MPC8266 “Power Quicc II” CPU, the **VRM-PMC-8266-OC3** is targeted at telecom applications with a need for a powerful and versatile platform, such as applications using SS7, ISDN, ATM, VoP or any combination of these protocols. As the MPC8266 processor supports IMA (Inverse Multiplexing for ATM), the **VRM-PMC-8266-OC3** is an ideal single board platform to interface between TDM standards such as E1/T1/J1 and cell oriented standards such as ATM.

The **VRM-PMC-8266-OC3** is a P1386.1/Draft 2.0 compatible PMC module that can be plugged onto any VME, CPCI or other carrier board offering a PMC extension slot. By the on-chip PCI bridge of the MPC8266, the **VRM-PMC-8266-OC3** is PCI Rev 2.2 compatible (32bit). The four primary rate line interfaces (E1/T1/J1) are driven using the Infineon PEB22554 “QuadFalc” framer, and are available on two standard RJ-45 connectors at the front panel. In addition to the four E1/T1/J1 lines, the **VRM-PMC-8266-OC3** offers an OC3 SDH/STM1 access using a standard optical fibre connector (SC duplex single or multi mode transceiver). Thus the **VRM-PMC-8266-OC3** is the generic platform for any implementation switching between the classic TDM streams as on E1/T1/J1 and the new generation of packetized data applications running on OC3, such as IMA in next generation mobile networks, i.e. UMTS. The onboard OKI CT812 H.110 bus controller offers access to the H.110 TDM bus and its SC Bus subset on the PMC P14 multi-purpose I/O connector. Equipped with up to 256MB DRAM and either 16 or 32MB onboard erasable Flash-Memory, the **VRM-PMC-8266-OC3** is optimized to meet the performance and memory requirements of state-of-the-art communication protocols and applications.

Communication protocols like SS7, ISDN, are available as binary firmware images as well as operating system independent source code licenses. By default, these firmware protocols run on the well proven real time kernel OK-1, which is optionally available in source code. Also available are BSP's for other operating systems such as VxWorks.



Features:

- CPU:** Motorola MPC8266 “PowerQuicc” at 166-300MHz
- PCI Interface and Compliance:** MPC8266 on-chip PCI Bridge (33MHz/66MHz), PCI Rev 2.2
- H.110:** OKI CT812, H.110 on PMC P14 connector
- DRAM:** 32-256MB SDRAM (PC-100, 64 bit) installed in SODIMM slot
- Flash PROM:** 16 or 32MB Flash PROM (32bit)
- Line Interface:** four primary rate E1/T1/J1 lines (1.431) on standard RJ45 connectors at front panel supplied by Infineon PEB22554 “QuadFalc”
- Networking:** OC3 SDH/STM1 optical fibre on standard connector at front panel (SC duplex single or multi mode transceiver)
- Indicator LEDs:** 4 software programmable LEDs at the front panel
- Operating System Support and Firmware:** OK-1, VxWorks, LINUXSS7, ISDN. IMA driver and others
- Power Consumption:** 3.3V 0.5A (max), 5.5V 0.8A (max)
- Environmental:** Temperature (operating): 0°C to +60°C with forced air cooling
 Temperature (storage): -40°C to +85°C
 Relative Humidity: 10% to 90% at +55°C (non condensing)
- Standard Compliance:** P1386 and P1386.1/Draft 2.0